

Fig. 1
PRIOR ART

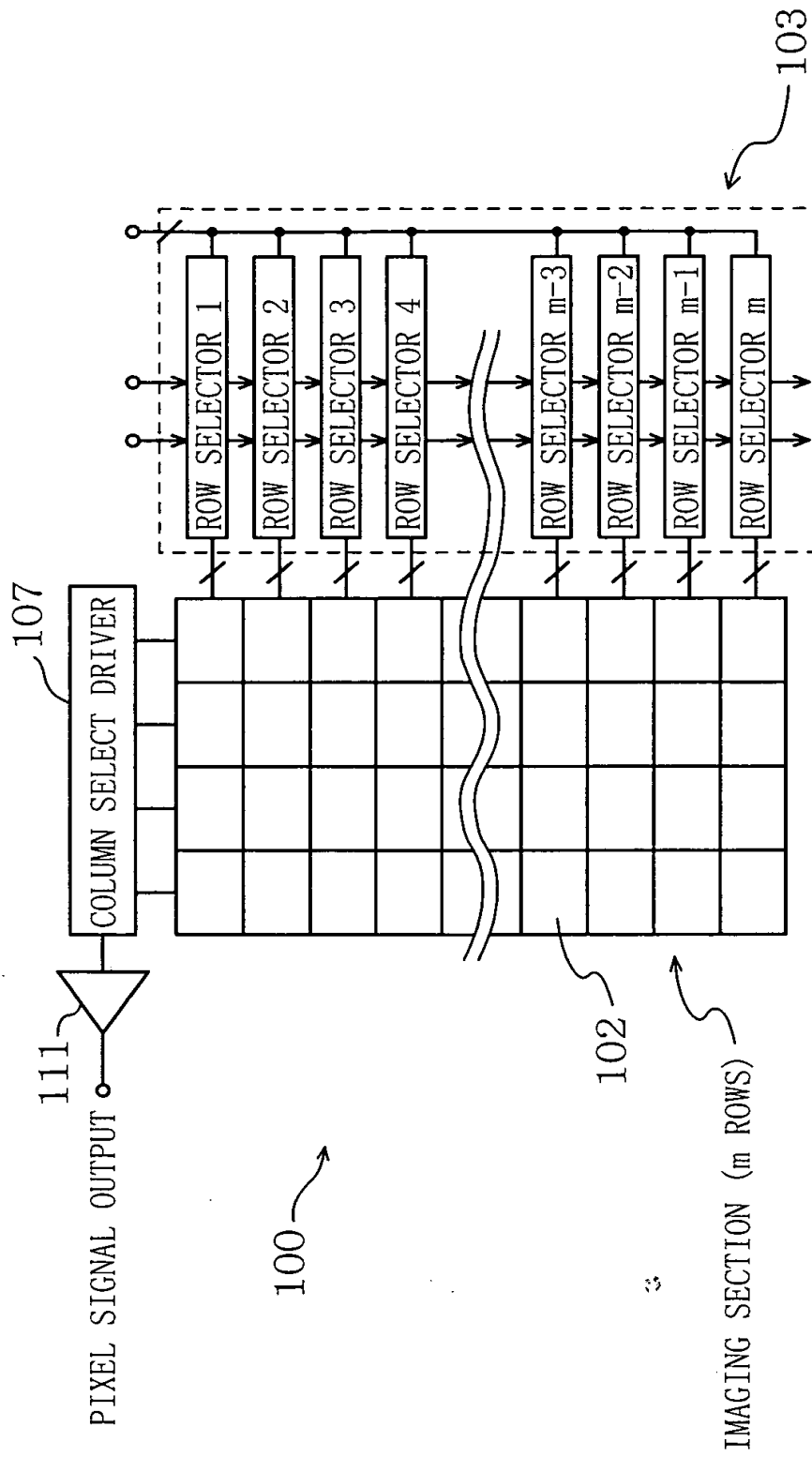


Fig. 3

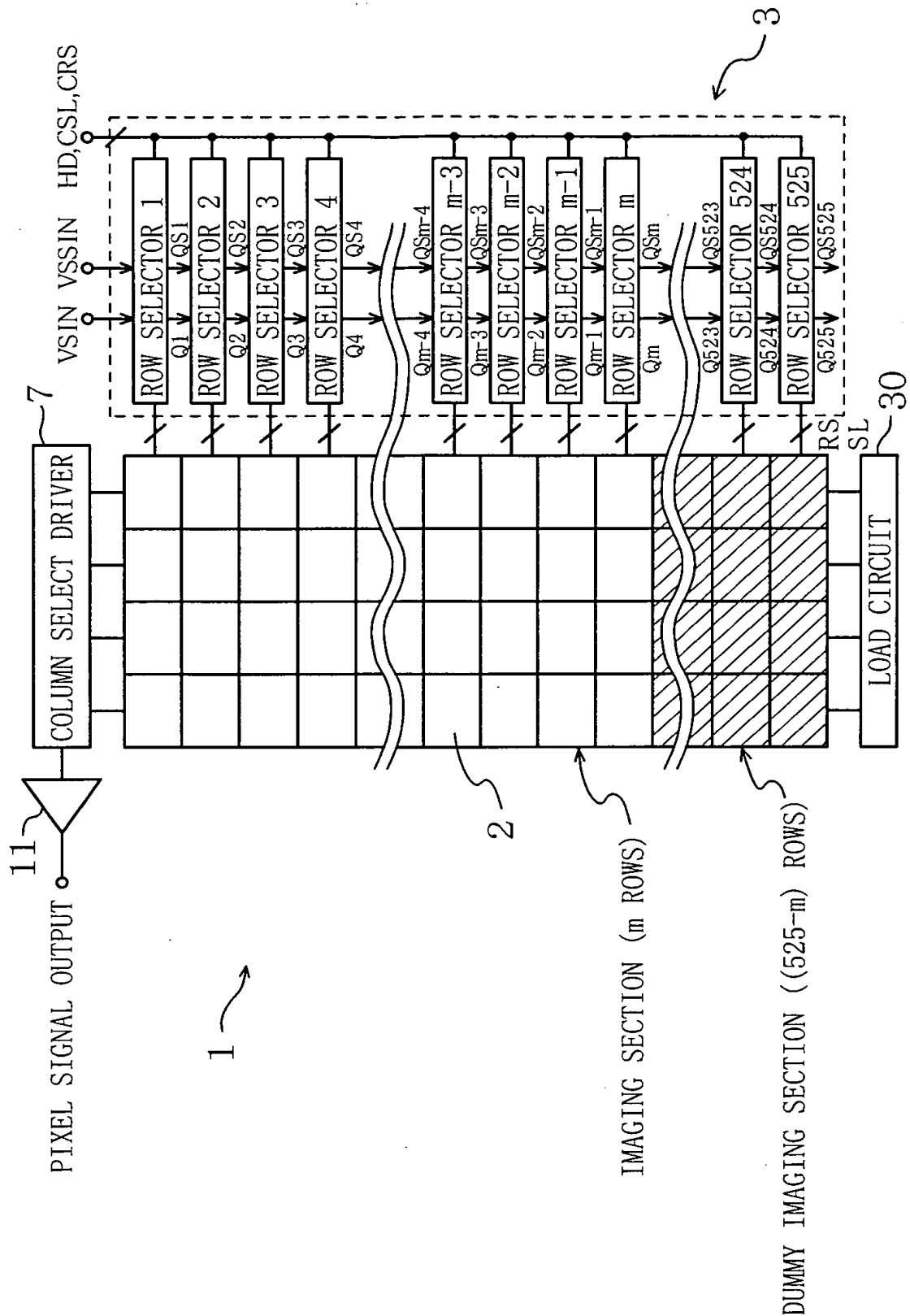


Fig. 4

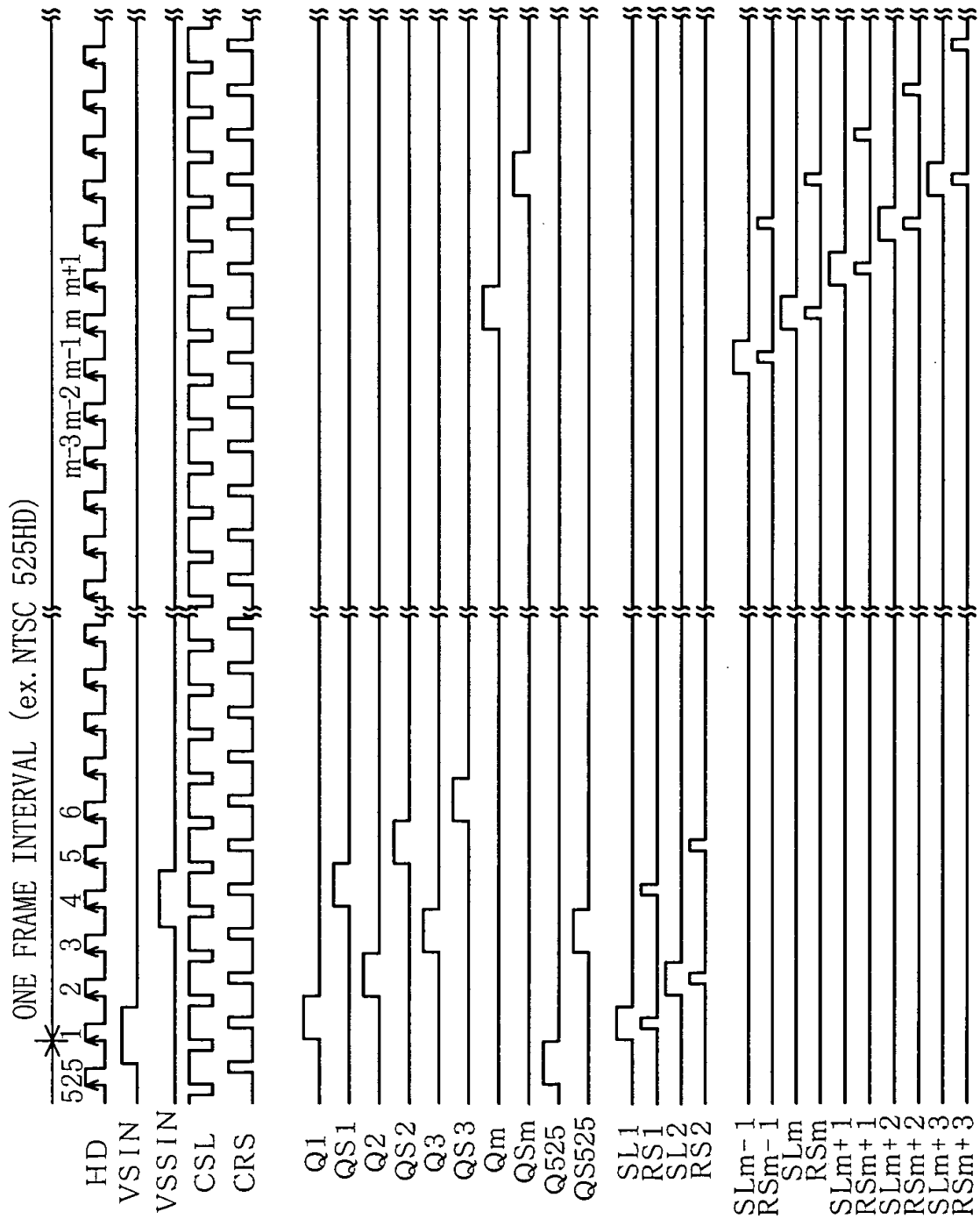


Fig. 6

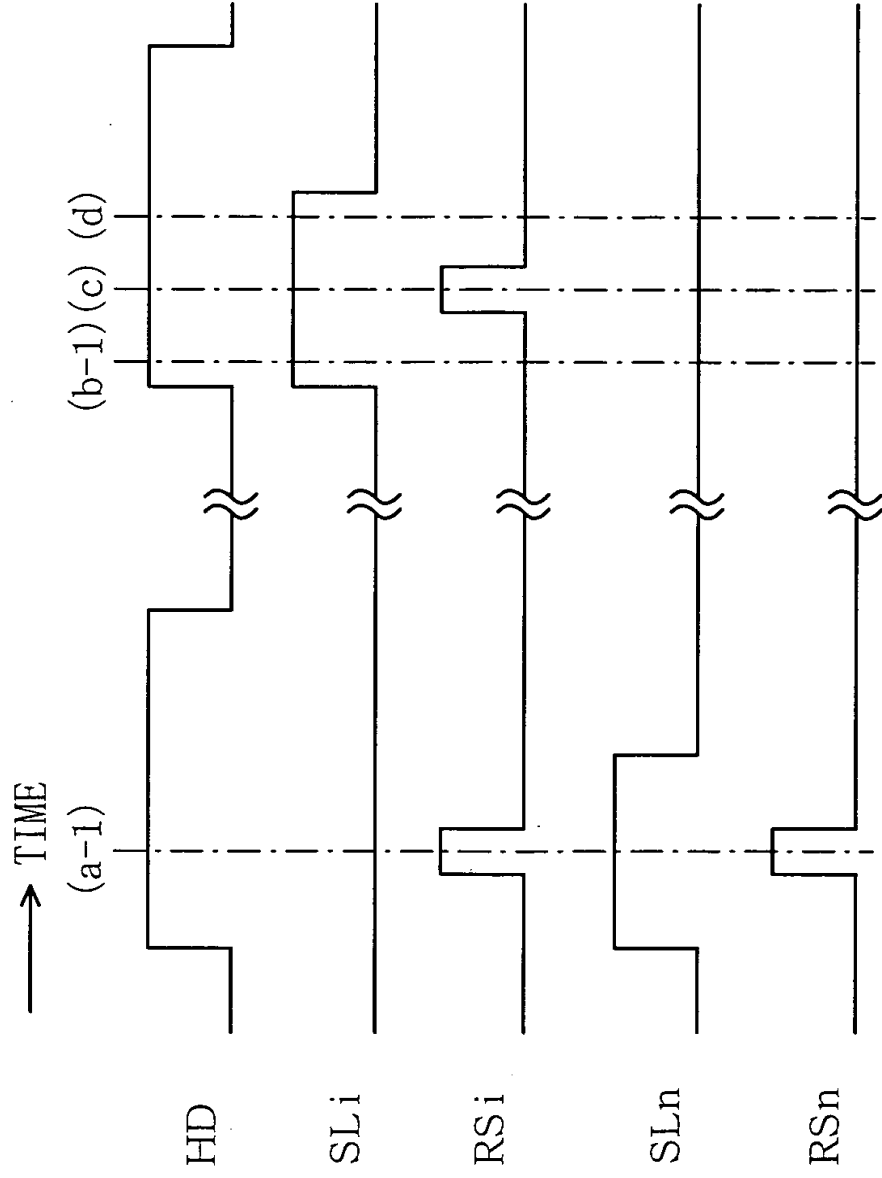
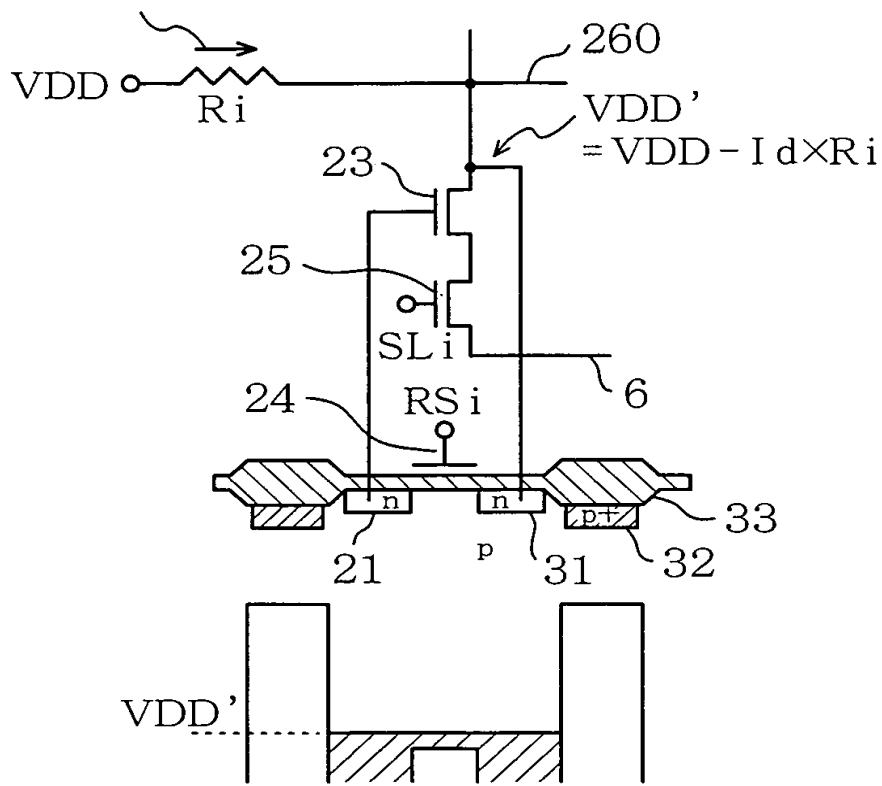


Fig. 7

n^{th} ROW SOURCE FOLLOWER CURRENT : I_d



RSi : ON

SLi : OFF

SLn : ON A ROW SELECT

TRANSISTOR TURNED ON

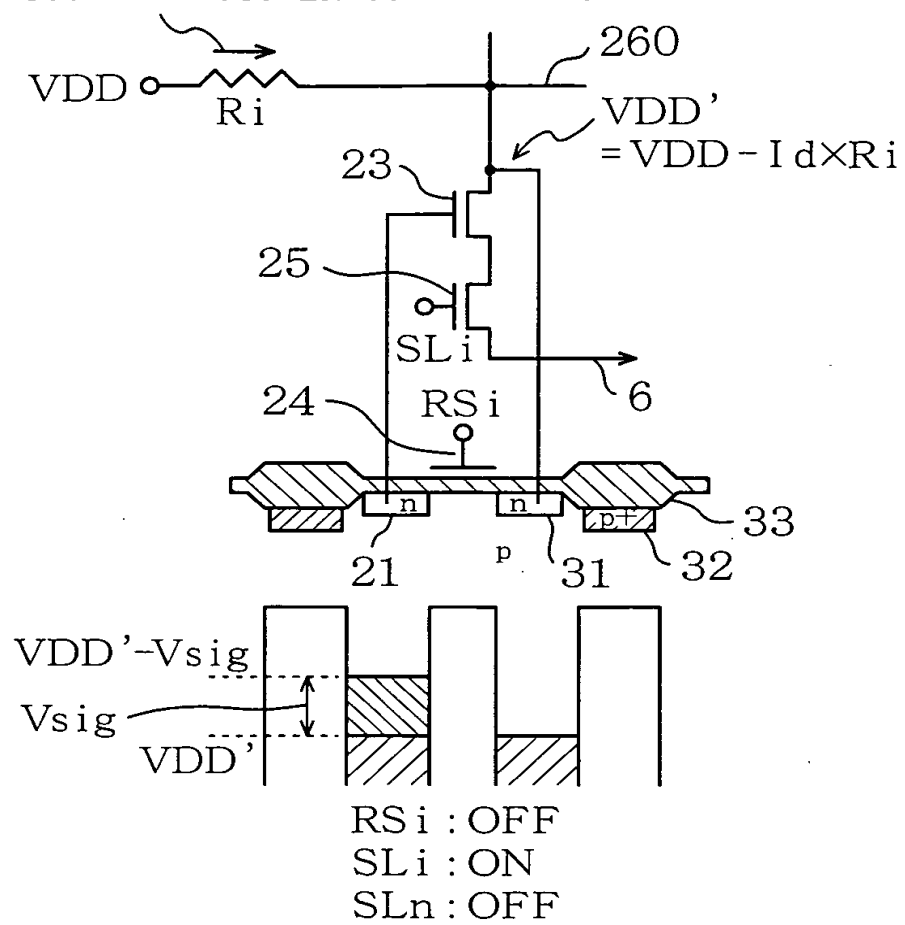
$$i^{\text{th}} \text{ ROW SOURCE FOLLOWER CURRENT : } I_d$$


Fig. 9

i^{th} ROW SOURCE FOLLOWER CURRENT : I_d

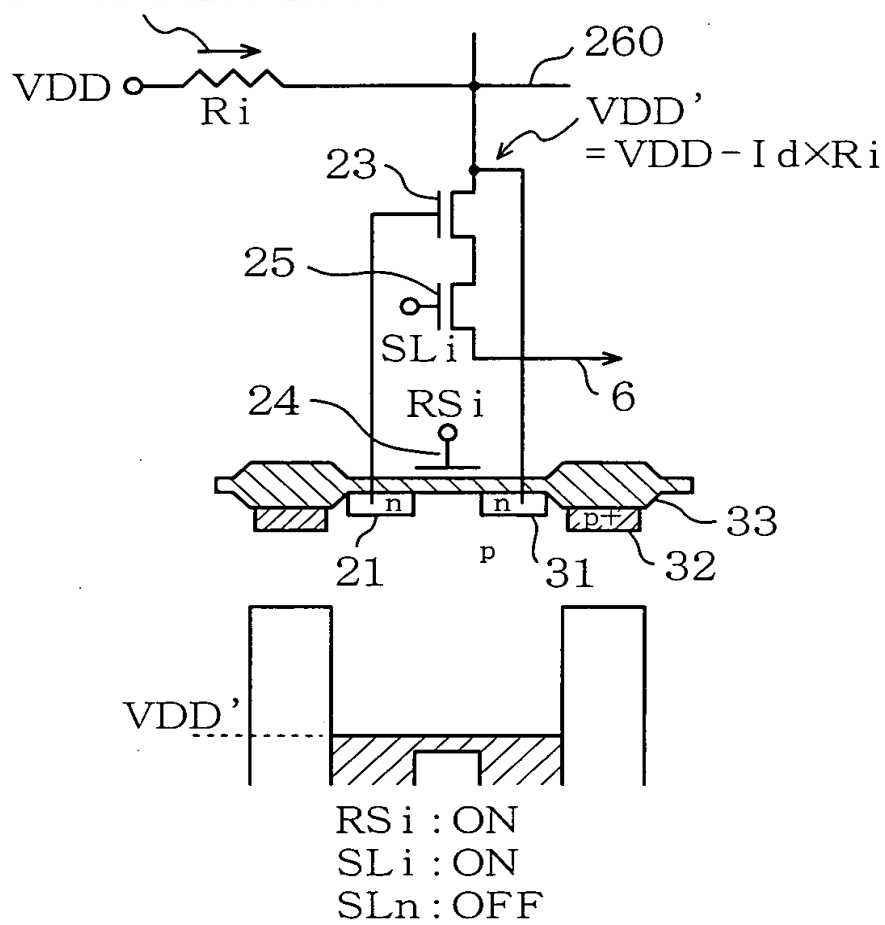


Fig. 10

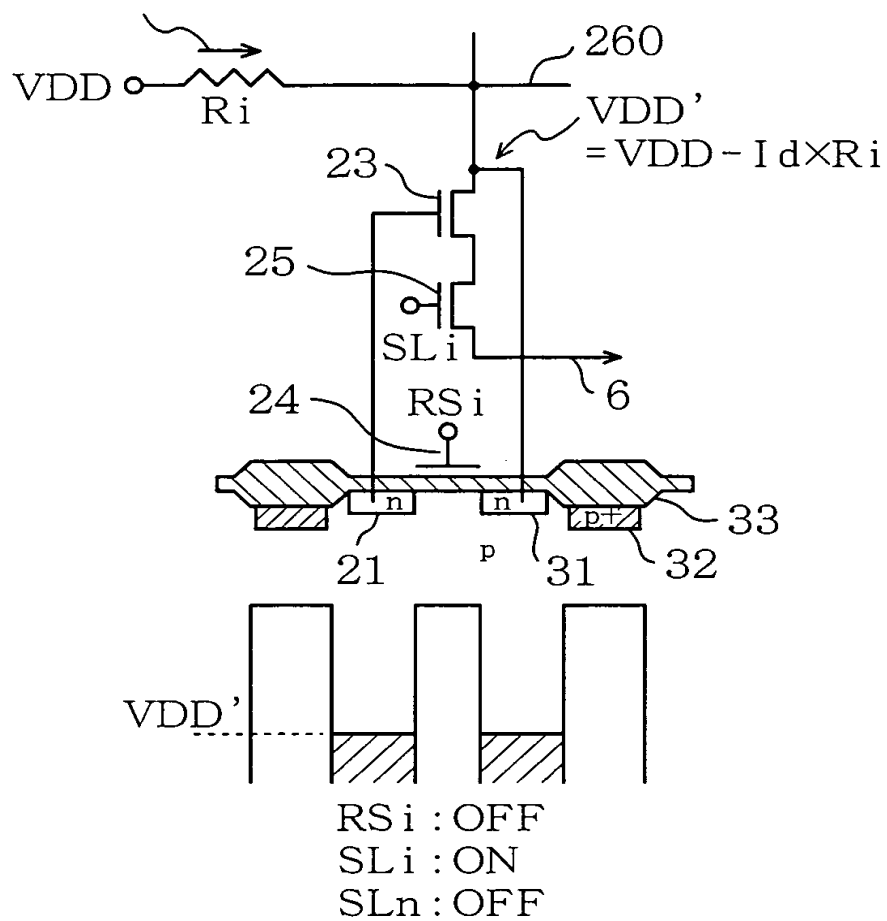
$$i^{\text{th}} \text{ ROW SOURCE FOLLOWER CURRENT : } I_d$$


Fig. 11

PRIOR ART

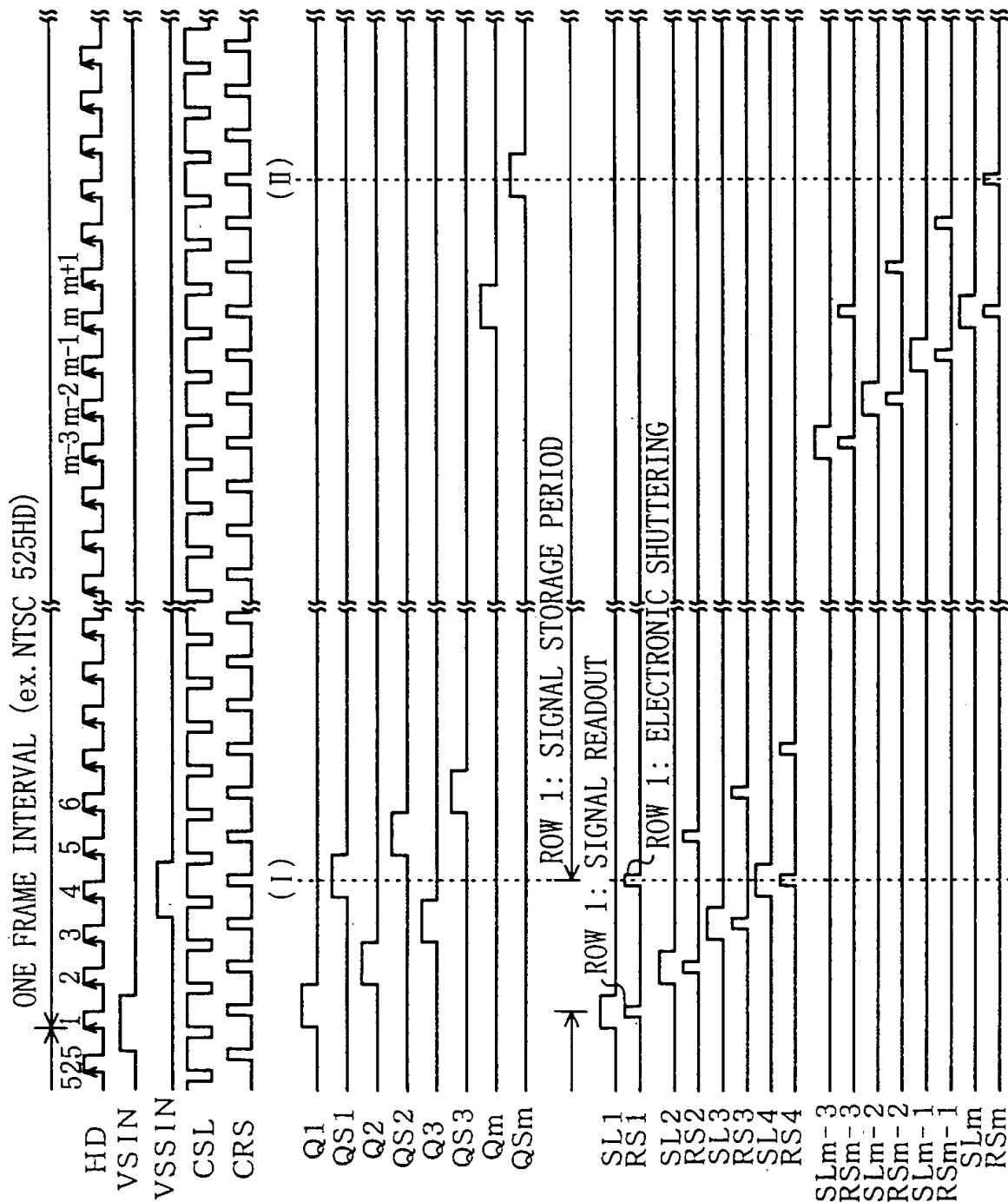
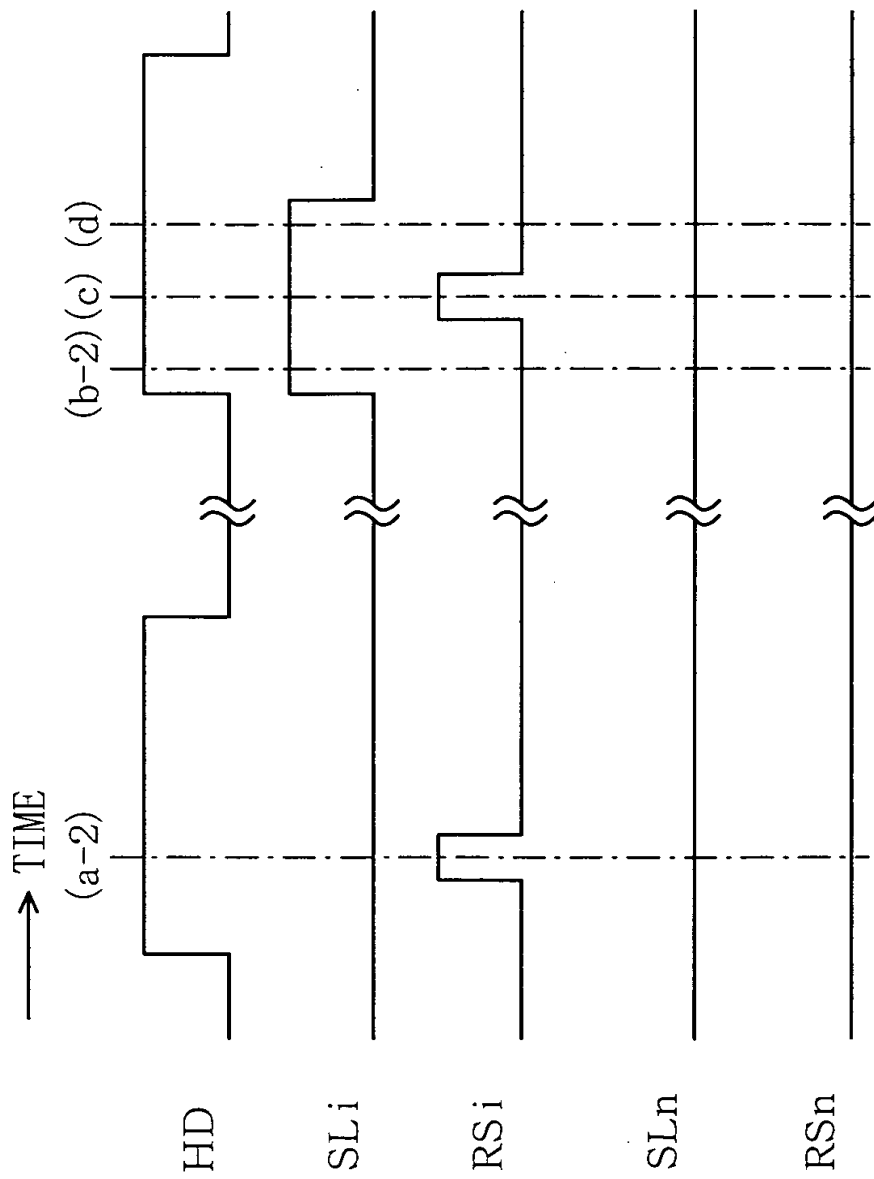


Fig. 12
PRIOR ART



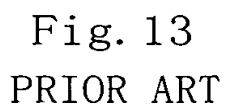


Fig. 16

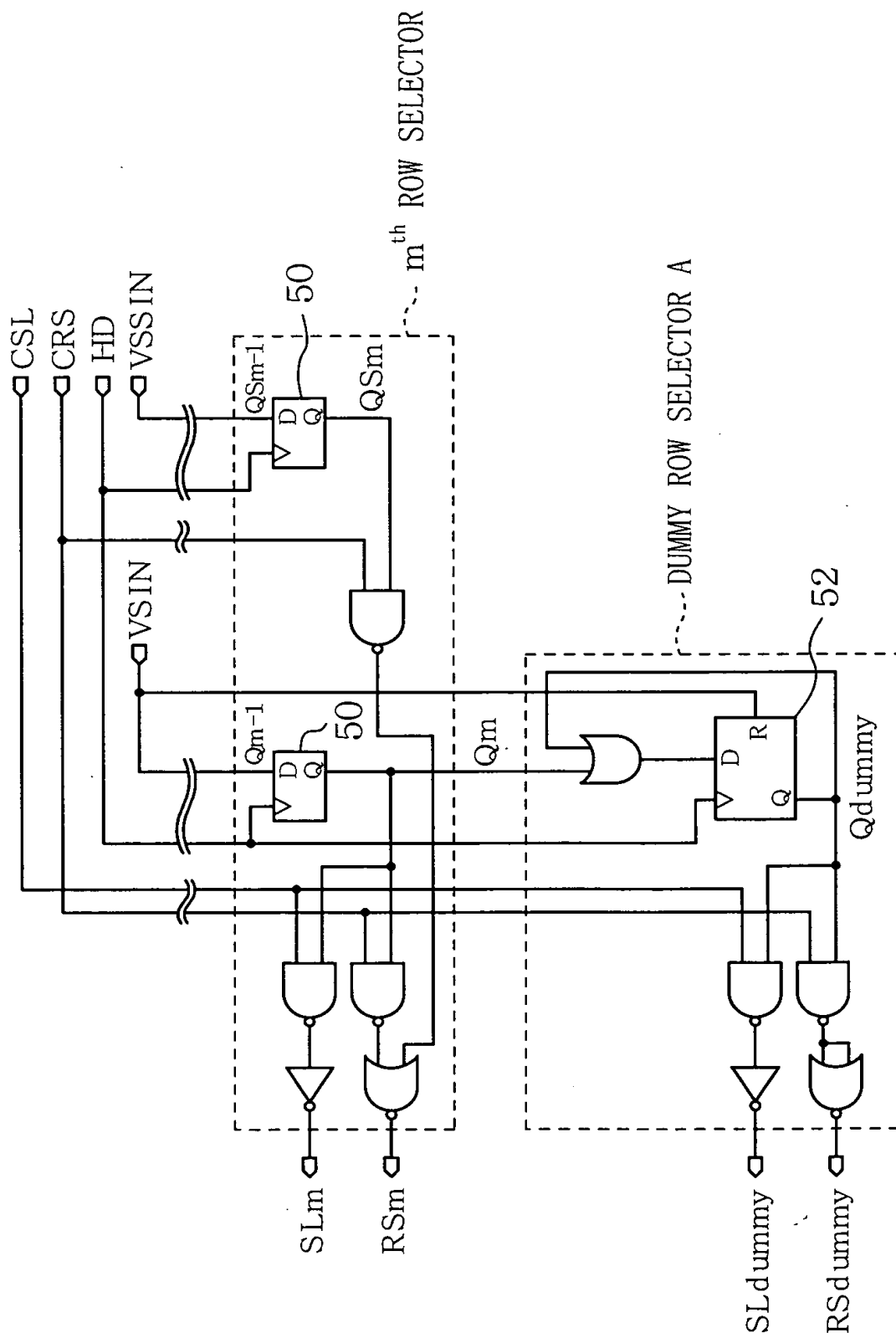
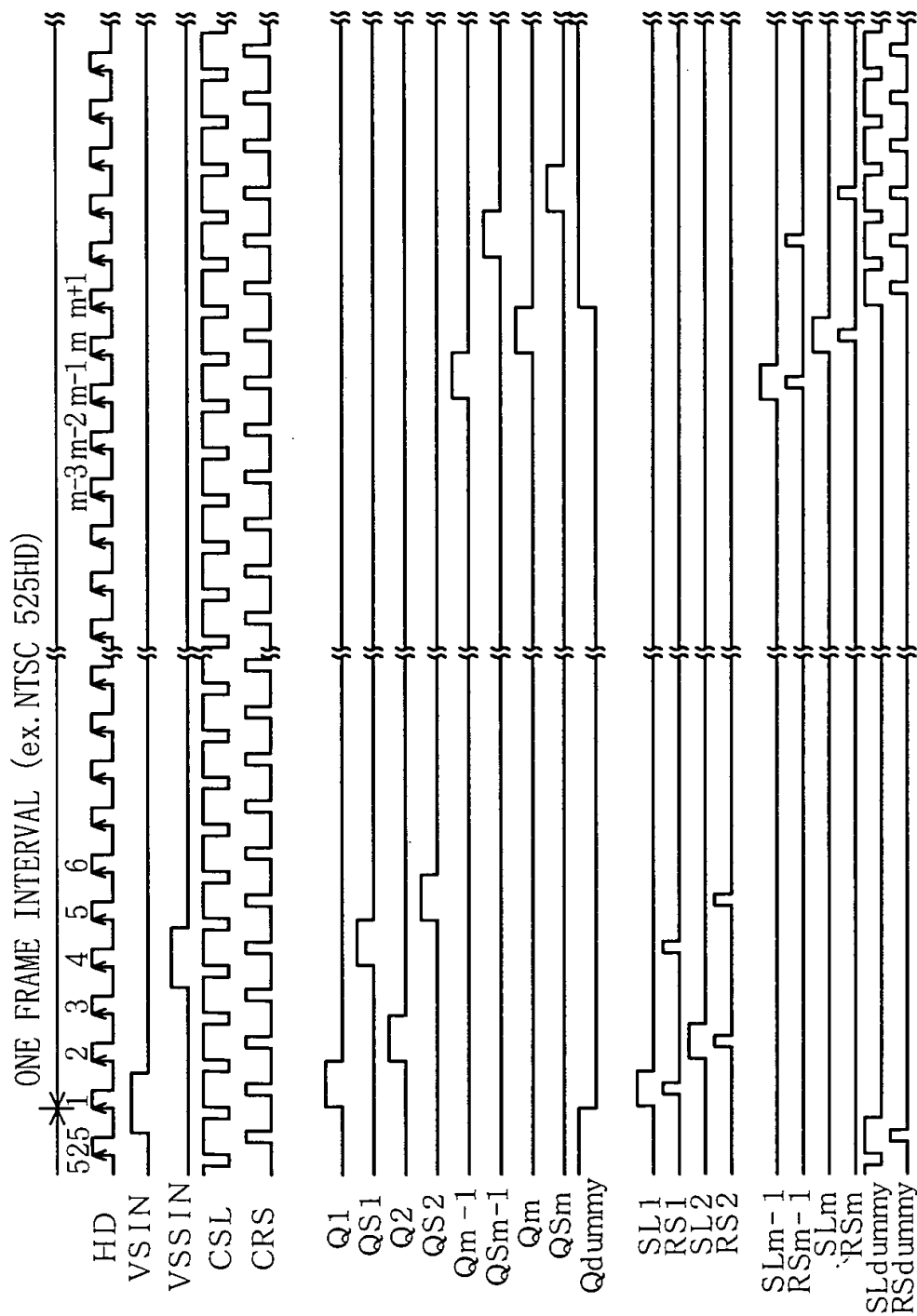


Fig. 17



Fi. 18

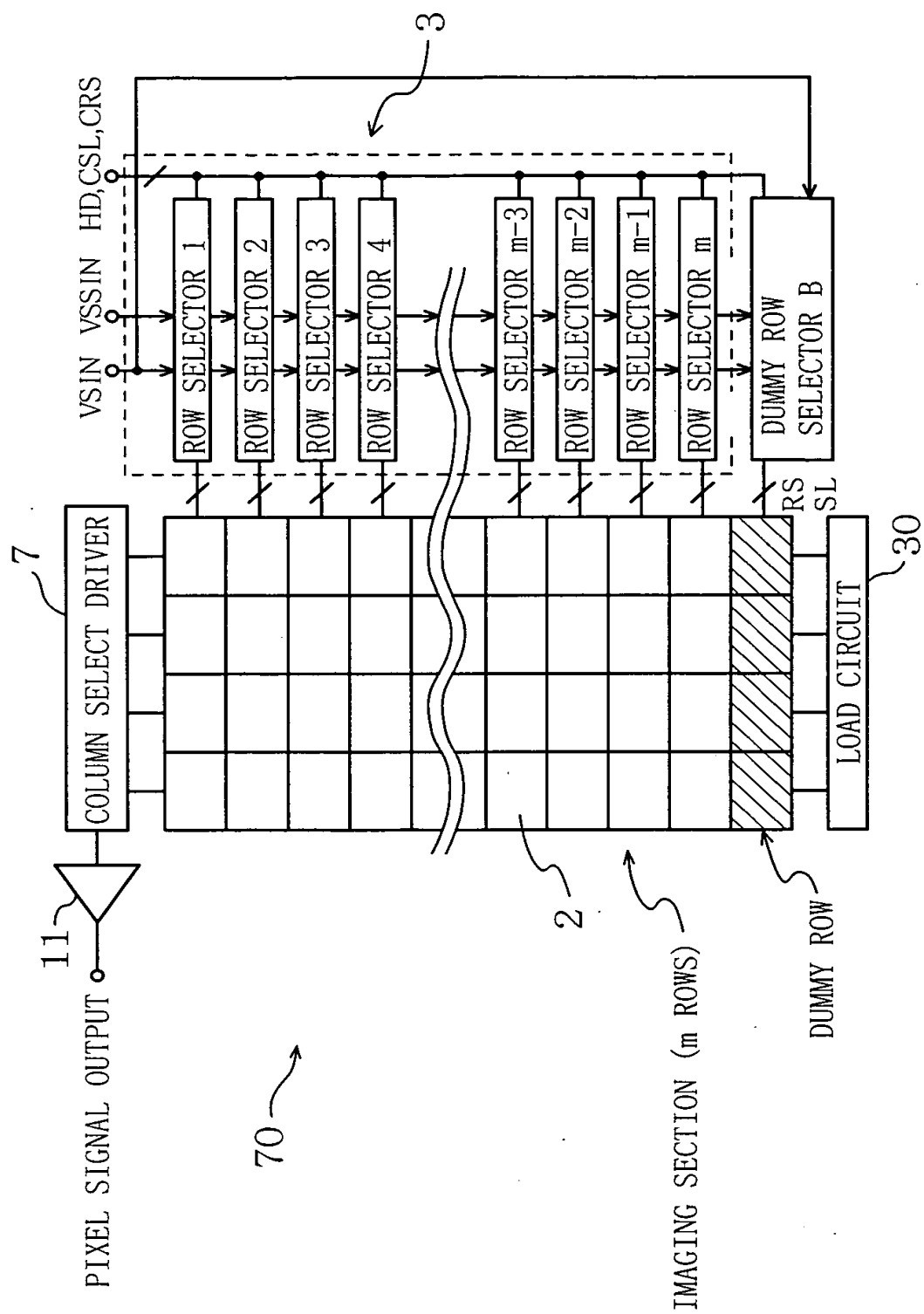


Fig. 19

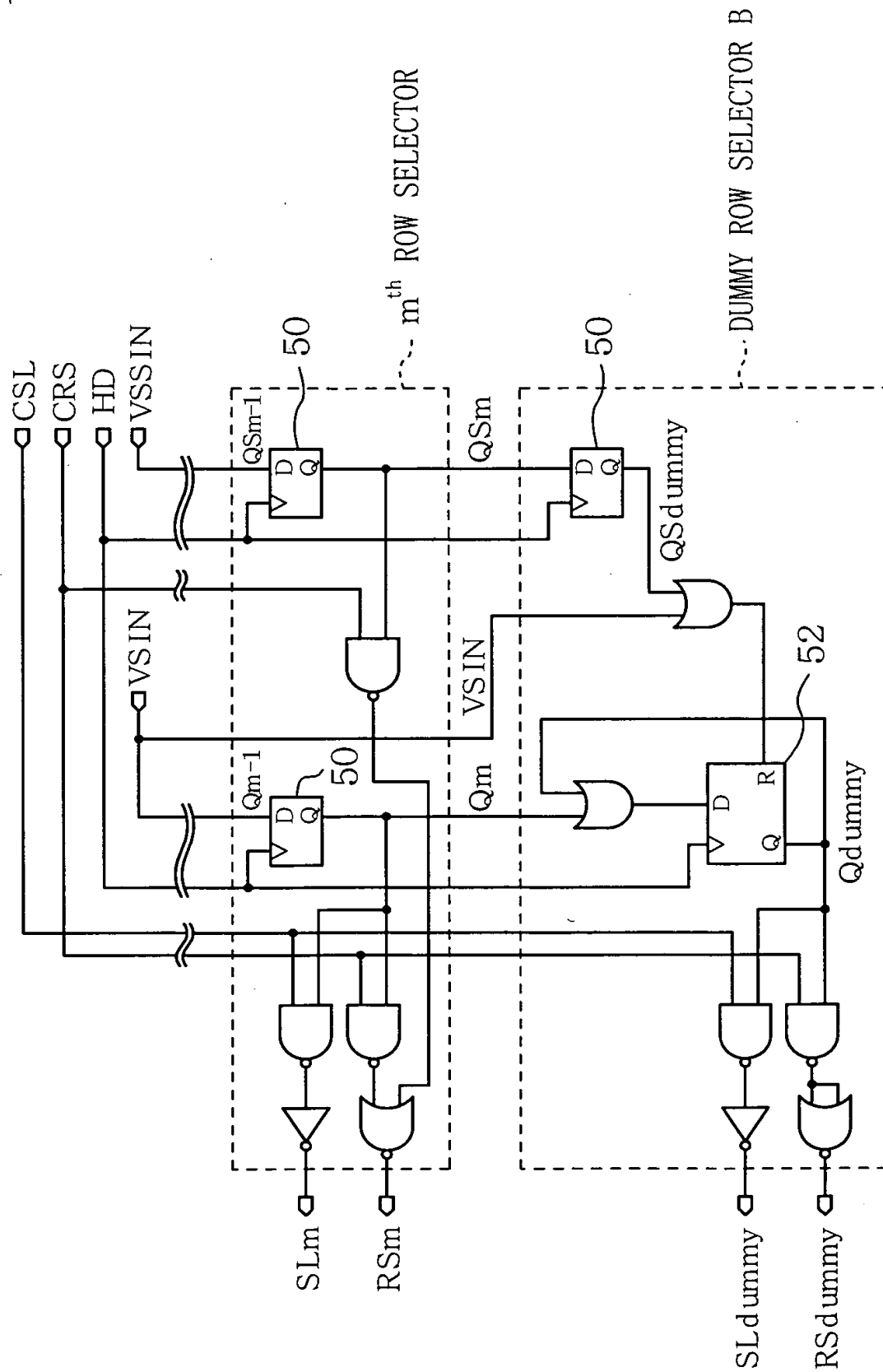


Fig. 20

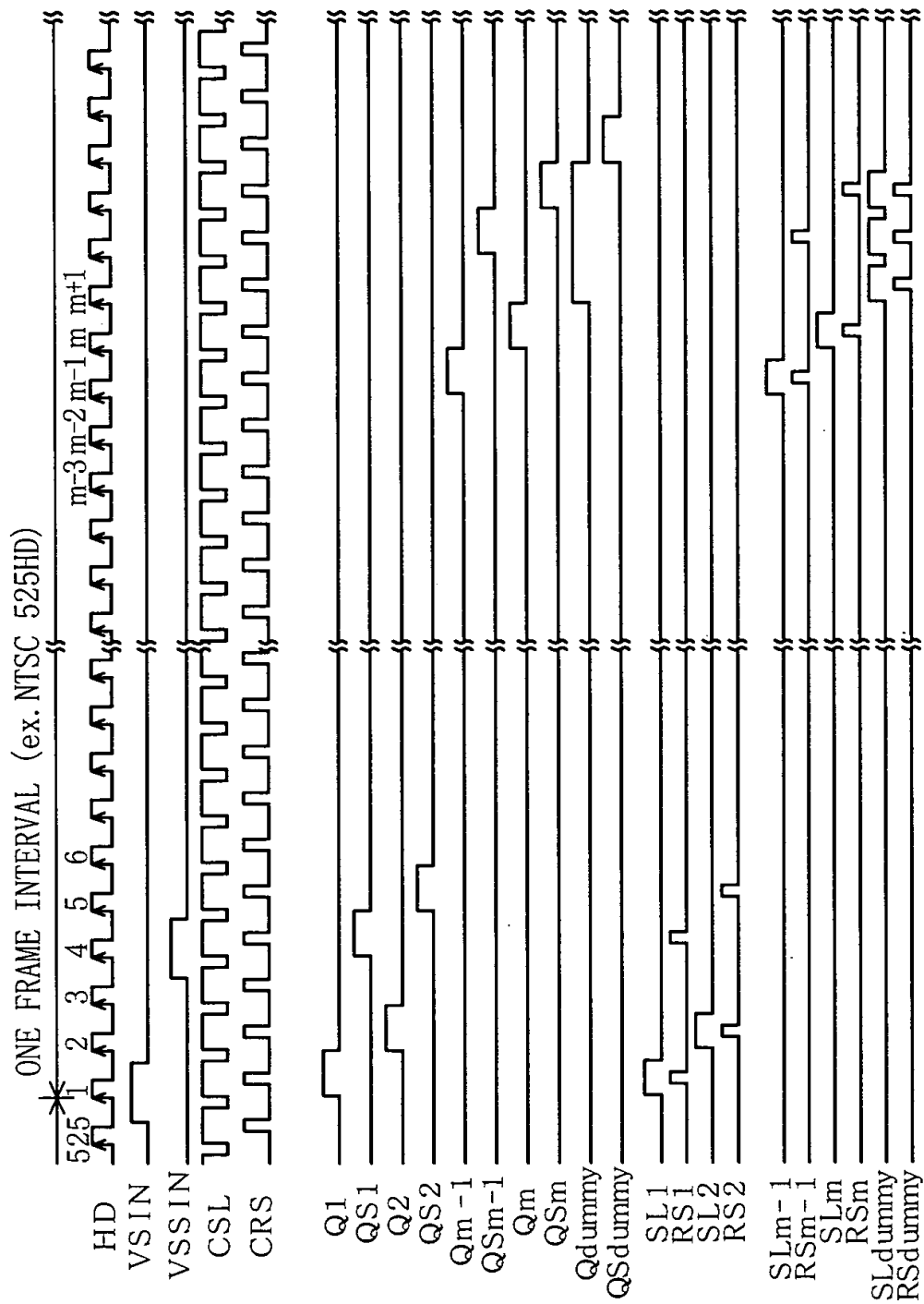


Fig. 21

